

**Amendments to the Specification:**

Please replace paragraph [0037] with the following amended paragraph:

[0037] (ii) in the event of illicit manipulation of the component 200 and/or unauthorized access to the component 200 being recognized: one or more preventing units V1, V2, V3, V4, V5, V6, V7 connected to the activating units A1, A2, A3, A4, A5 are activated, where

(ii.1) preventing unit V1 is arranged to prevent an internal oscillator from beginning to oscillate

(ii.2) preventing unit V2 is arranged to prevent an oscillator for an external clock signal from beginning to oscillate,

| (ii.3) preventing unit V3 is arranged to switch off a high-voltage limiter, in particular by means of permanent programming,

(ii.4) preventing unit V4 is arranged to prevent the build-up of a high voltage,

(ii.5) preventing unit V5 is arranged to reprogram the allocation of addresses and/or the allocation of data,

(ii.6) preventing unit V6 is arranged to load the memory element 210 of the component 200 with illicit values of data, and/or

(ii.7) preventing unit V7 is arranged to switch on an increased current drain in the operating state or the quiescent state, and

(iii) the component 200 is deactivated and/or the component 200 is destroyed by means of preventing units V1, V2, V3, V4, V5, V6, V7.